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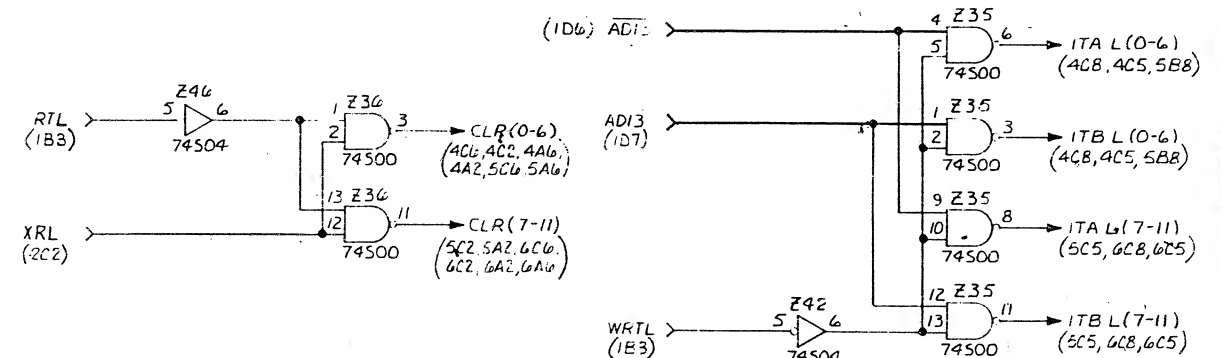
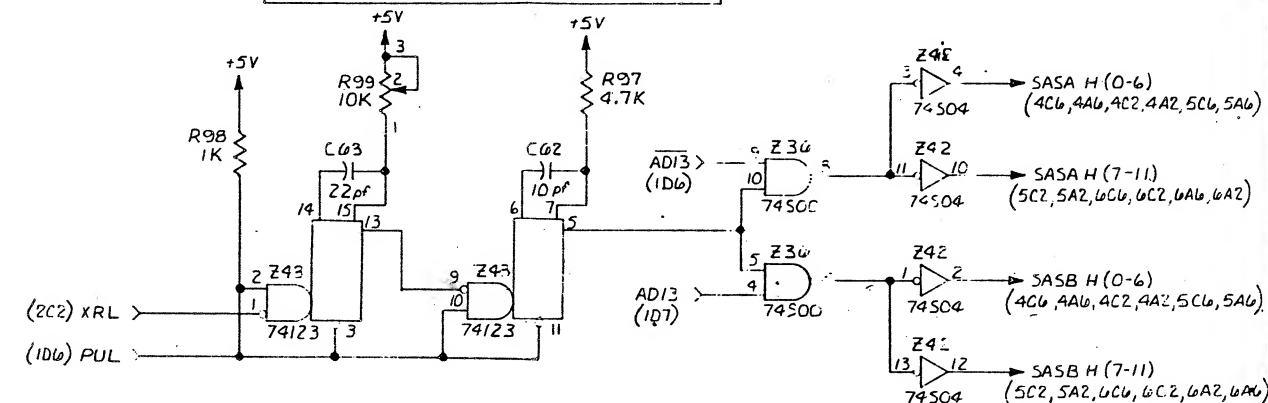
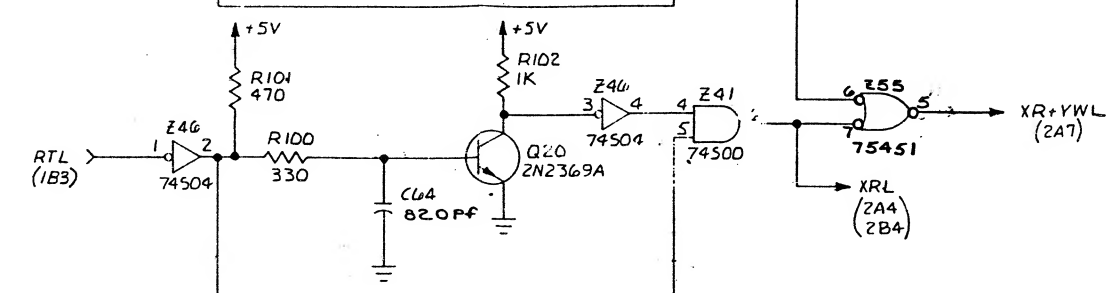
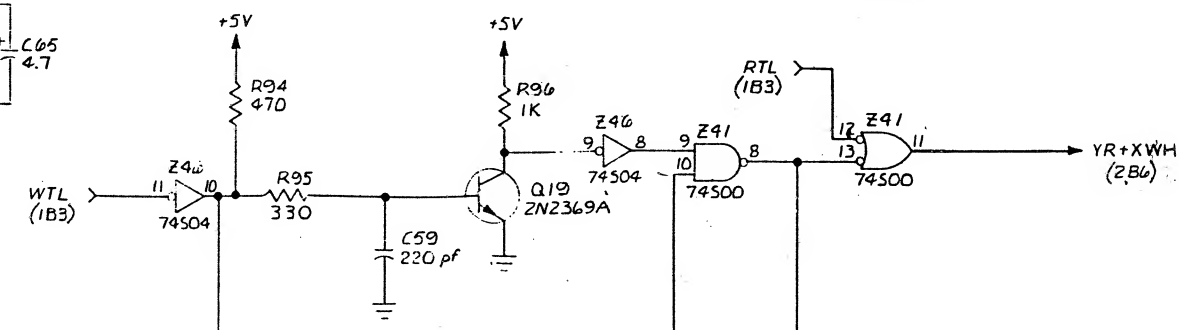
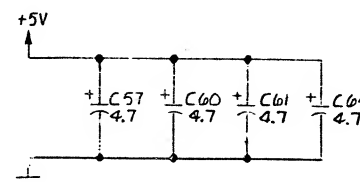
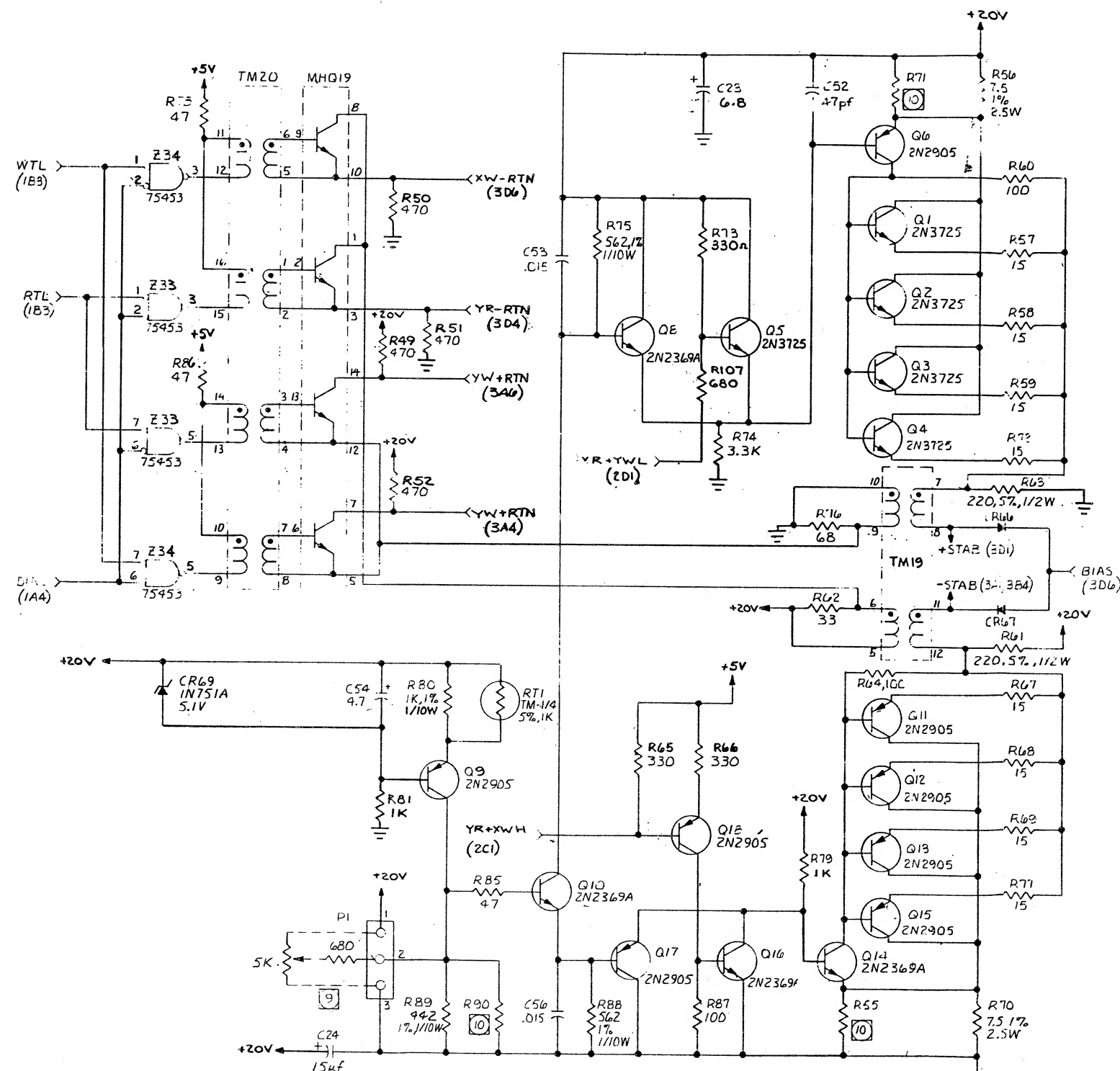
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LAST DESIGNATION USED	
RESISTOR	R109
RESISTOR MODULE	RM25
TRANSISTOR	Q21
TRANSISTOR MODULE	MHQ19
DIODE	CR69
DIODE MODULE	CRM20
CAPACITOR	C71
INTEGRATED CIRCUIT	Z57
TRANSFORMER MODULE	TM20
THERMISTOR	RT1

REF DESIG	GATES USED PER TOTAL	PART NUMBER
Z38	3/4	74500
Z40	5/6	74504
Z50	1/6	74504
Z56	1/6	74504
Z57	1/4	8837

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
-	-	SEE SHEET ONE	-	-



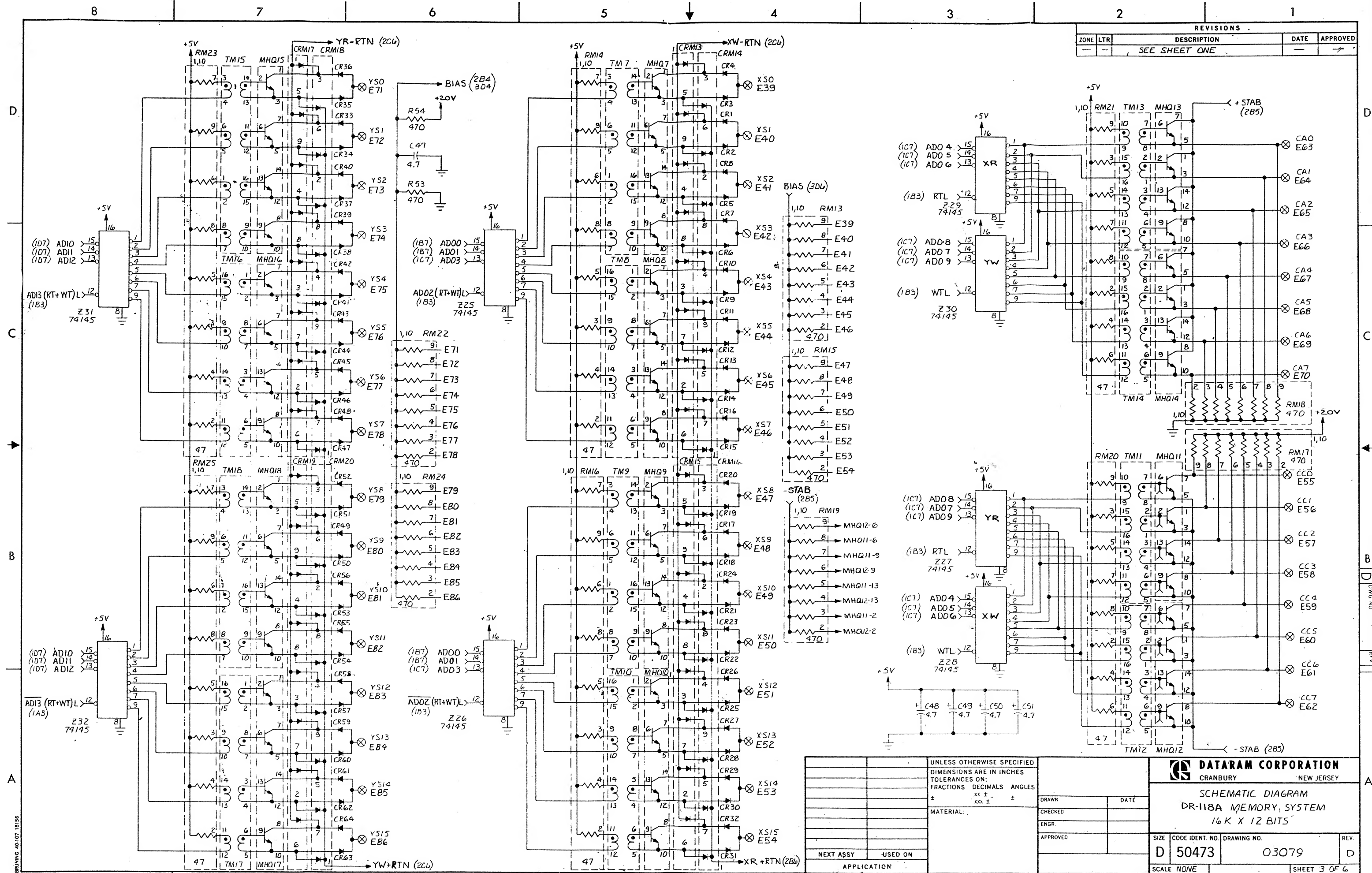
UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ON: FRACTIONS DECIMALS ANGLES ± .010 ± .005 ± .002		DRAWN		DATE	
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NEXT ASSY		APPROVED		DATE	
APPLICATION		SCALE NONE		SHEET 2 OF 6	

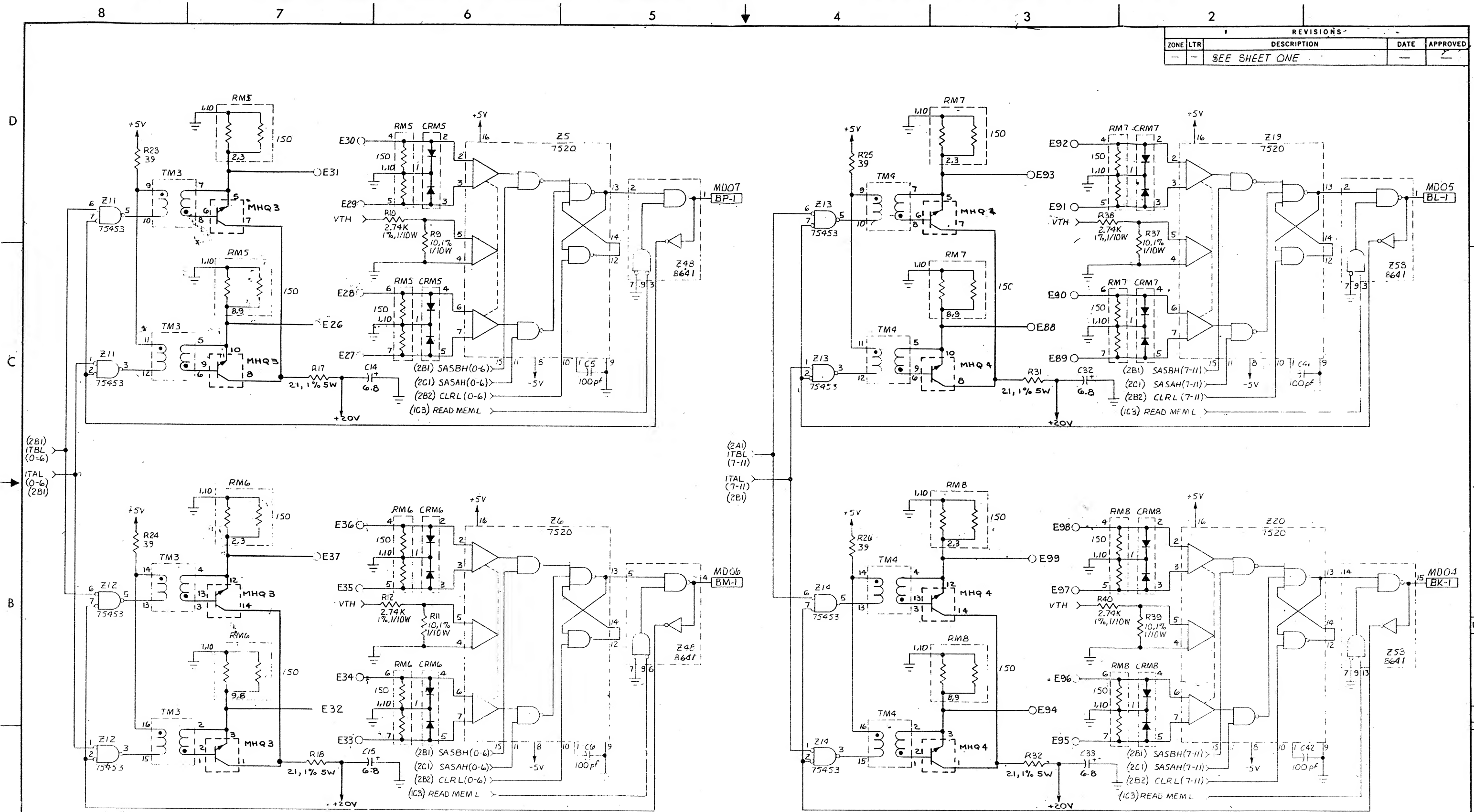
DATARAM CORPORATION
CRANBURY NEW JERSEY

SCHEMATIC DIAGRAM
DR-118A MEMORY SYSTEM
16K X 12 BITS

SIZE	CODE IDENT. NO.	DRAWING NO.	REV.
D	50473	03079	D

WHEN REFERRING TO THIS DRAWING STATE DRAWING NO., APPLICABLE REVISION LETTER IF ANY, AND DATE





REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
—	—	SEE SHEET ONE	—	—

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON: FRACTIONS DECIMALS ANGLES ± .xx ± .xxx ± ±		DRAWN CHECKED ENGR. APPROVED		DATE
MATERIAL:		SIZE D 50473		CODE IDENT. NO. 03079
NEXT ASSY USED ON APPLICATION		DRAWING NO. 03079		REV. D
		SCALE NONE		SHEET 5 OF 6

DATARAM CORPORATION
CRANBURY NEW JERSEY

SCHEMATIC DIAGRAM
DR118A MEMORY SYSTEM
16 K X 12 BITS

WHEN REFERRING TO THIS DRAWING STATE DRAWING NO., APPLICABLE REVISION LETTER IF ANY, AND DATE

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